

10

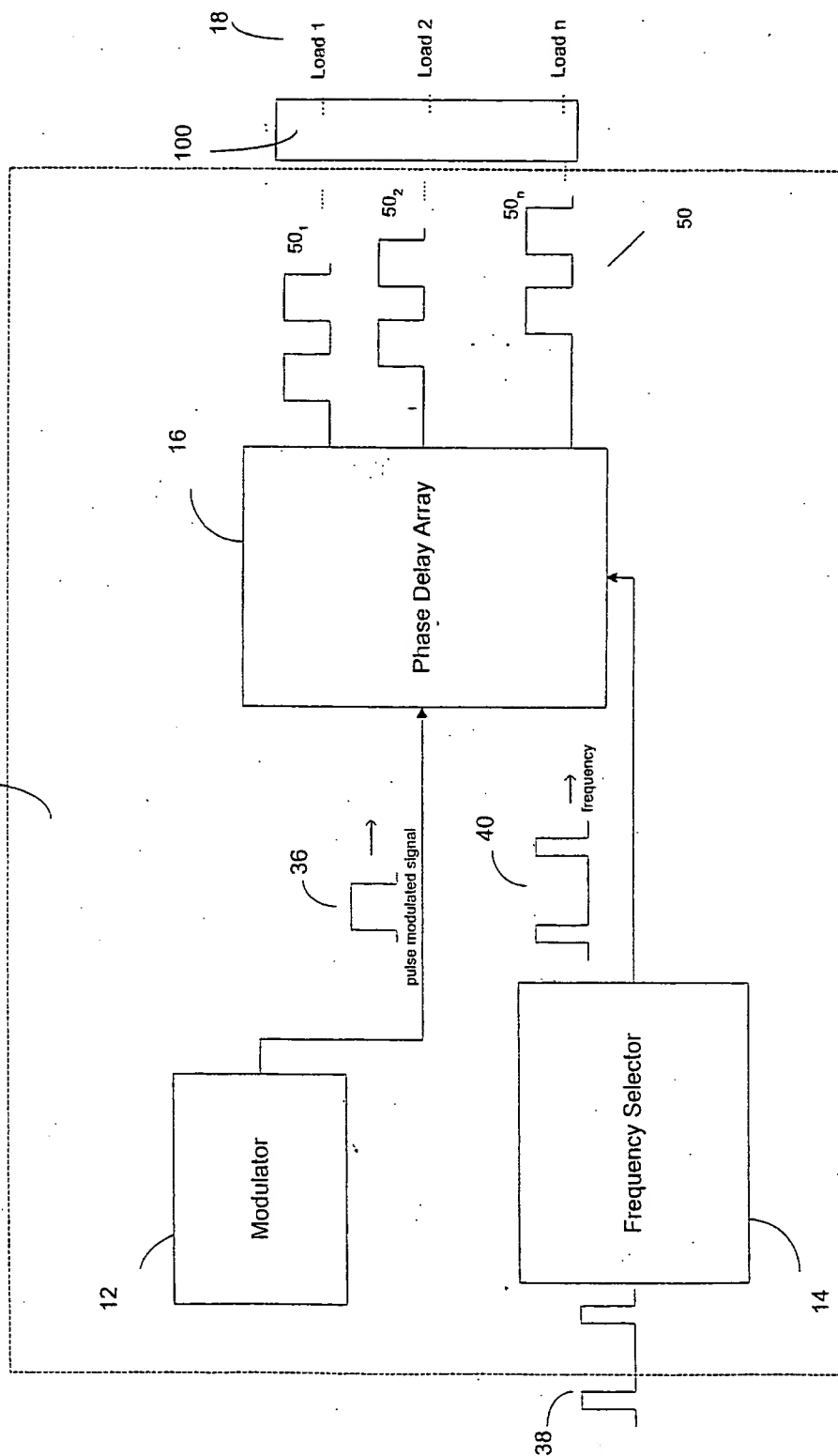


FIGURE 1

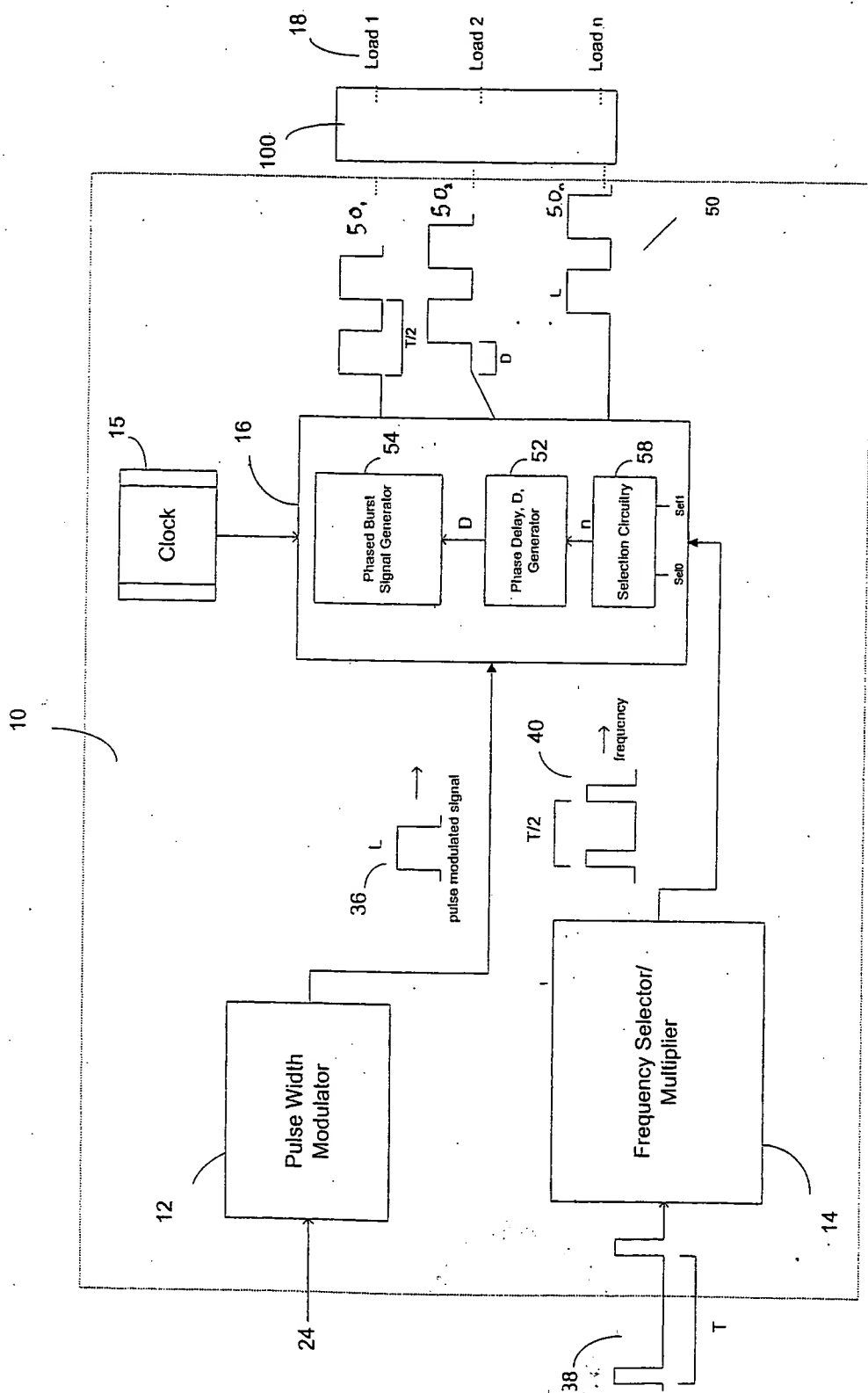


FIGURE 2

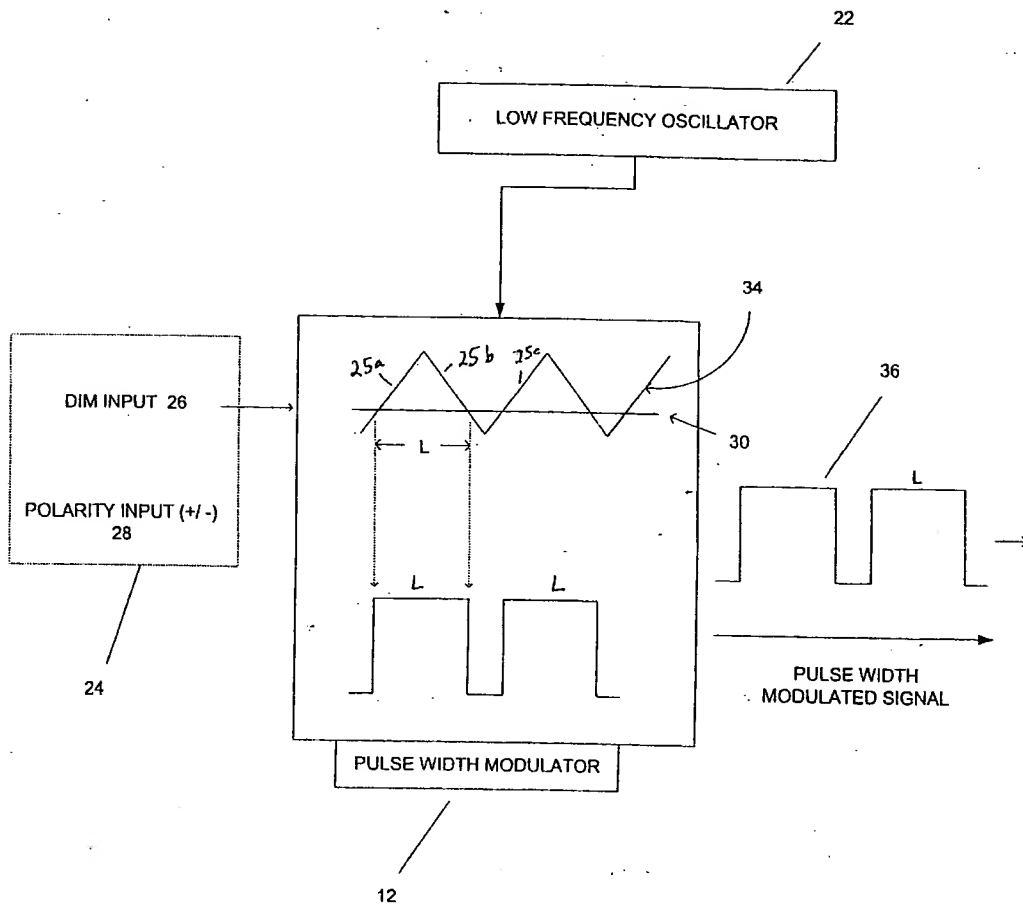


FIGURE 3

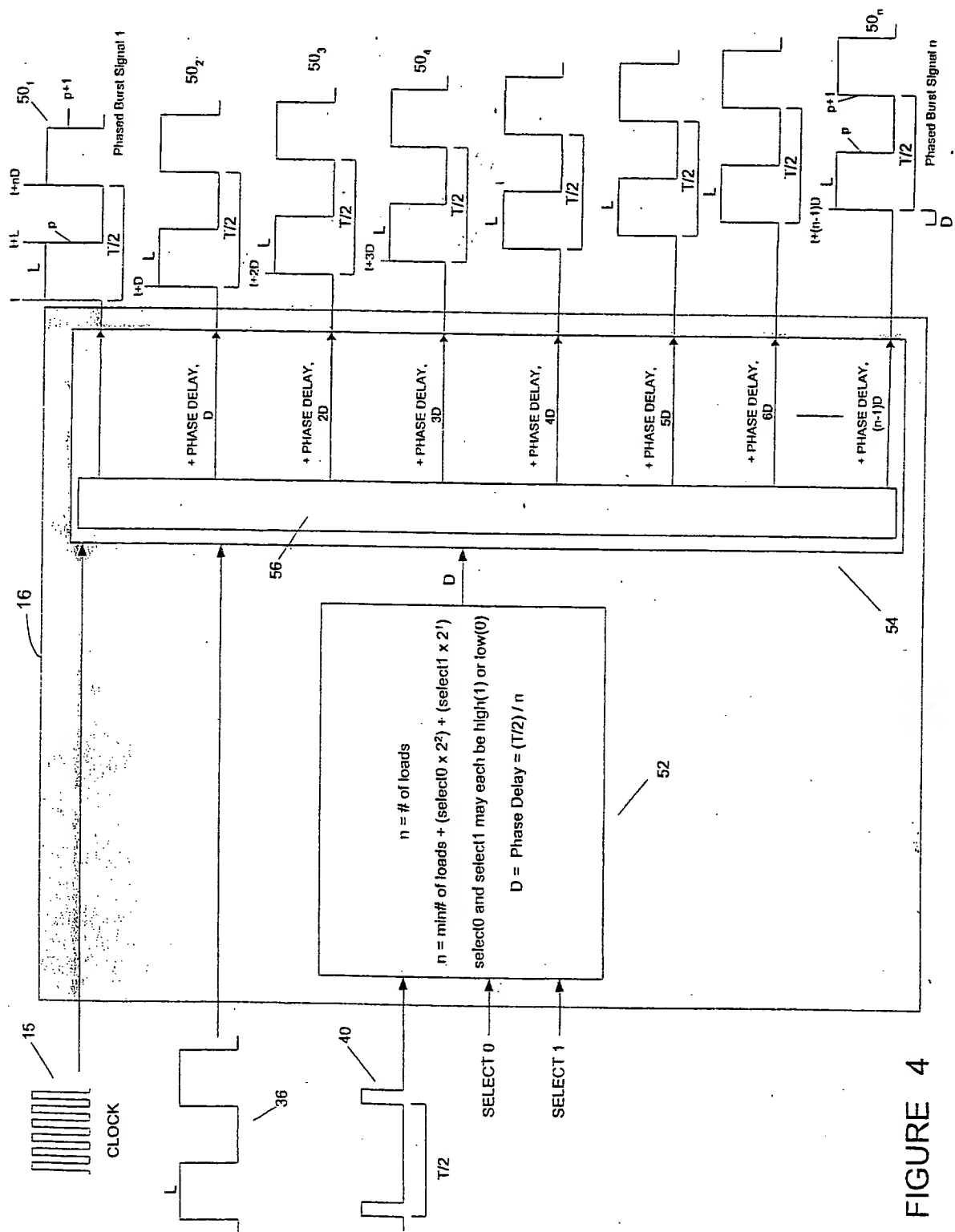


FIGURE 4

Sel0 (Select 0)	0	0	1	1
Sel1 (Select 1)	0	1	0	1
n (# of CCFLs)	6 (or min #)	8 (min # + 2)	10 (min # + 2 ²)	12 (min # + 2 + 2 ²)

FIGURE 5 (a)

Sel0	0	1	0	1	0	1	0	1
Sel1	0	0	1	1	0	0	1	1
Sel2	0	0	0	0	1	1	1	1
n (# of loads)	min + 0	min + 2	min + 4	min + 6	min + 8	min + 10	min + 12	min + 14

FIGURE 5 (b)

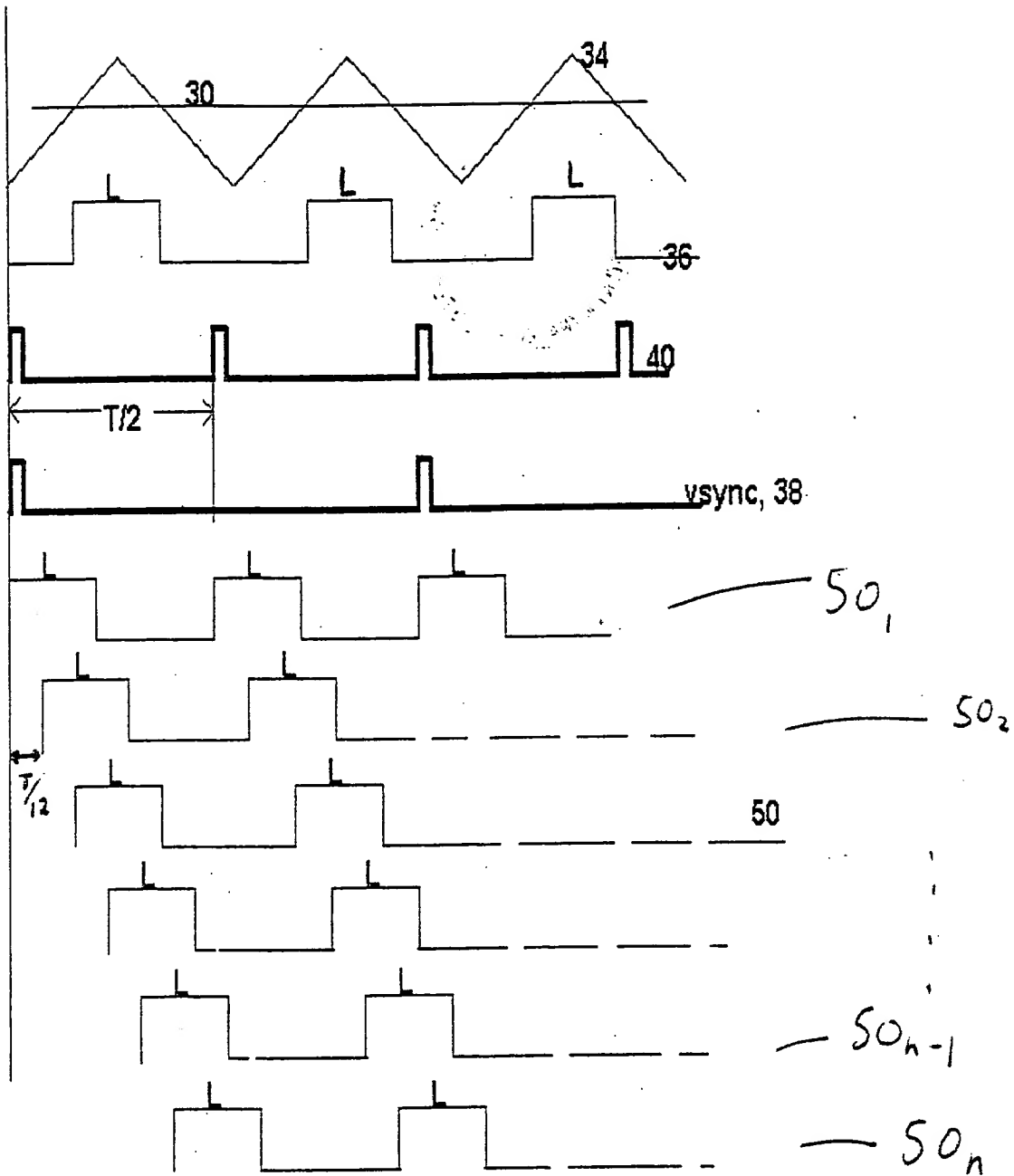


FIGURE 6

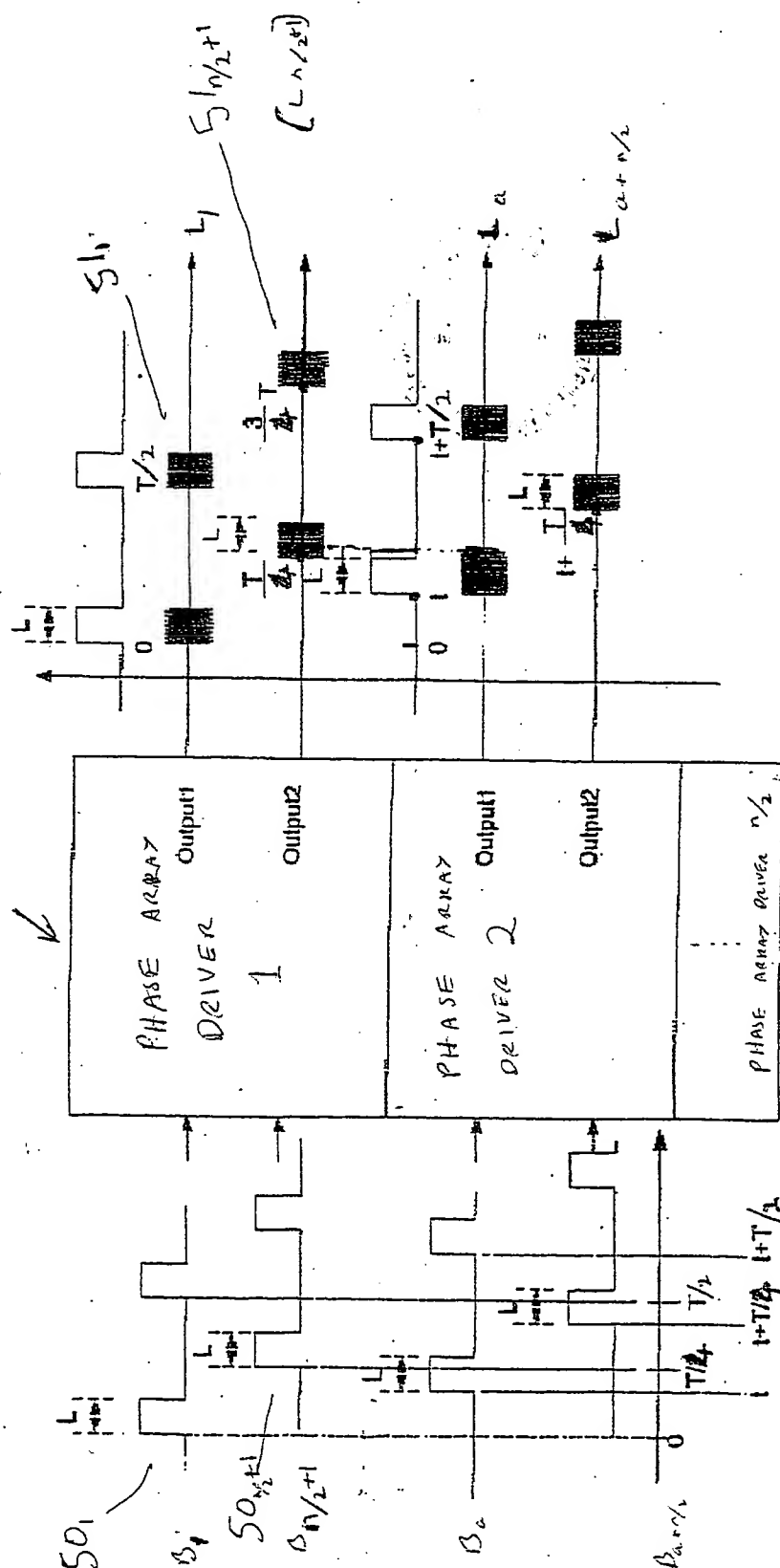


FIGURE 8

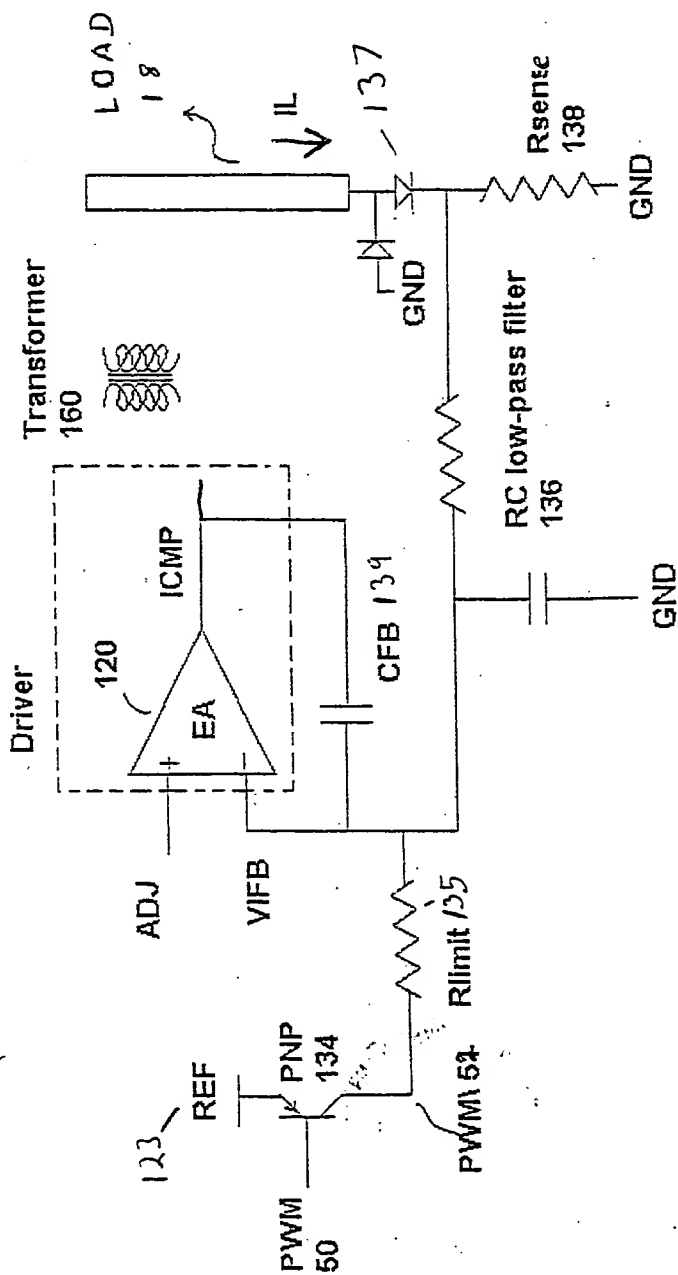


FIGURE 9

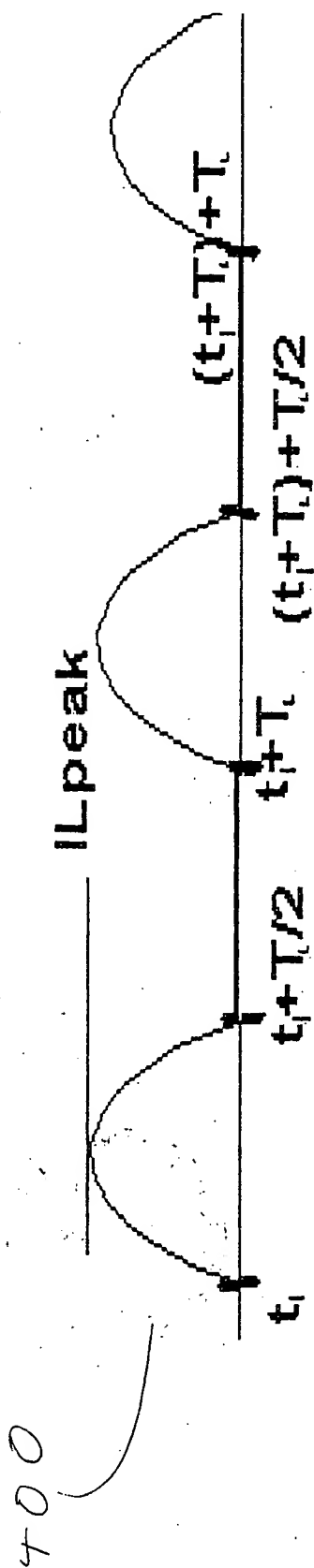


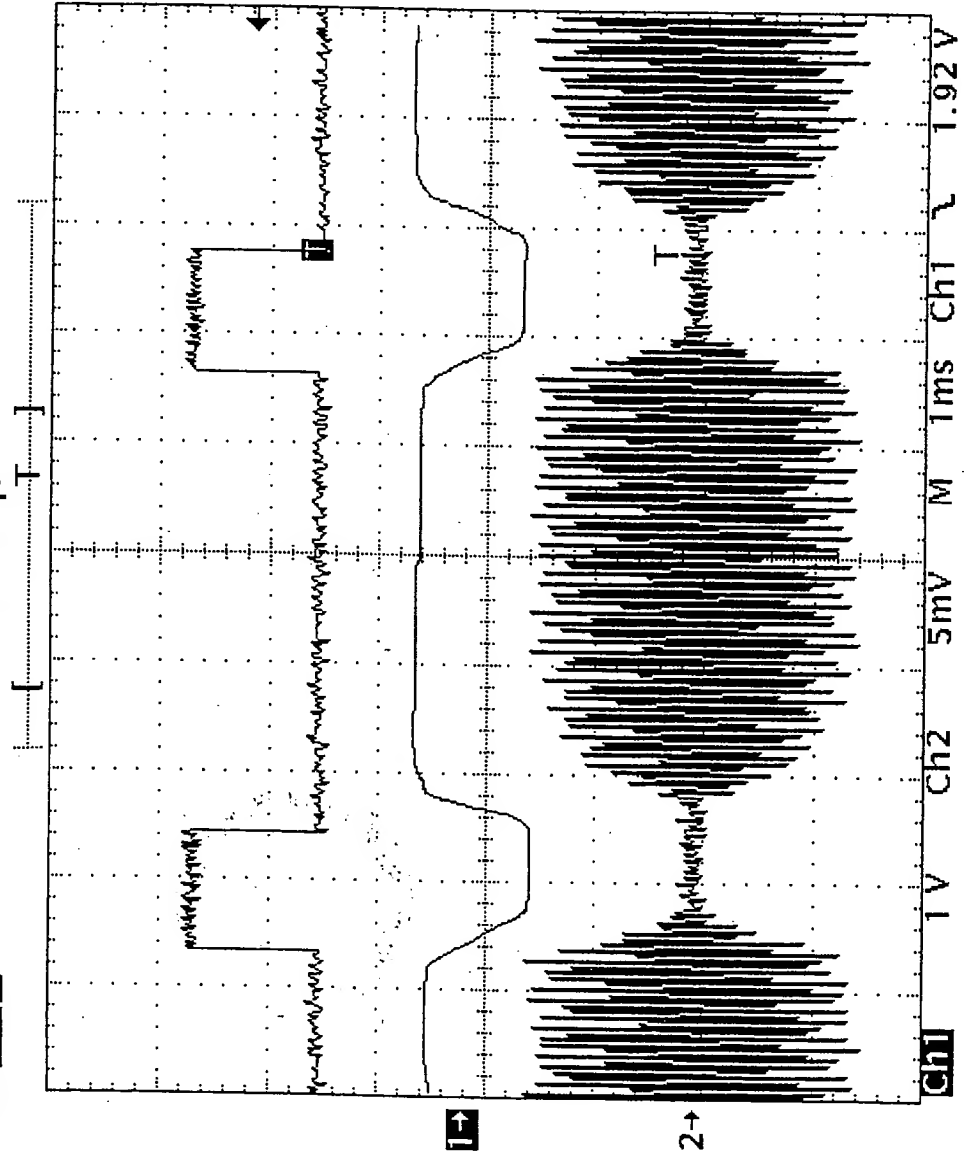
FIGURE 9a

Figure 10

10800 33023260

Tek **STOP** 50KS/s

201 Acqs



52

PWM

I CMP

Current [IL]

11 May 2000
11:44:20

FIGURE 11

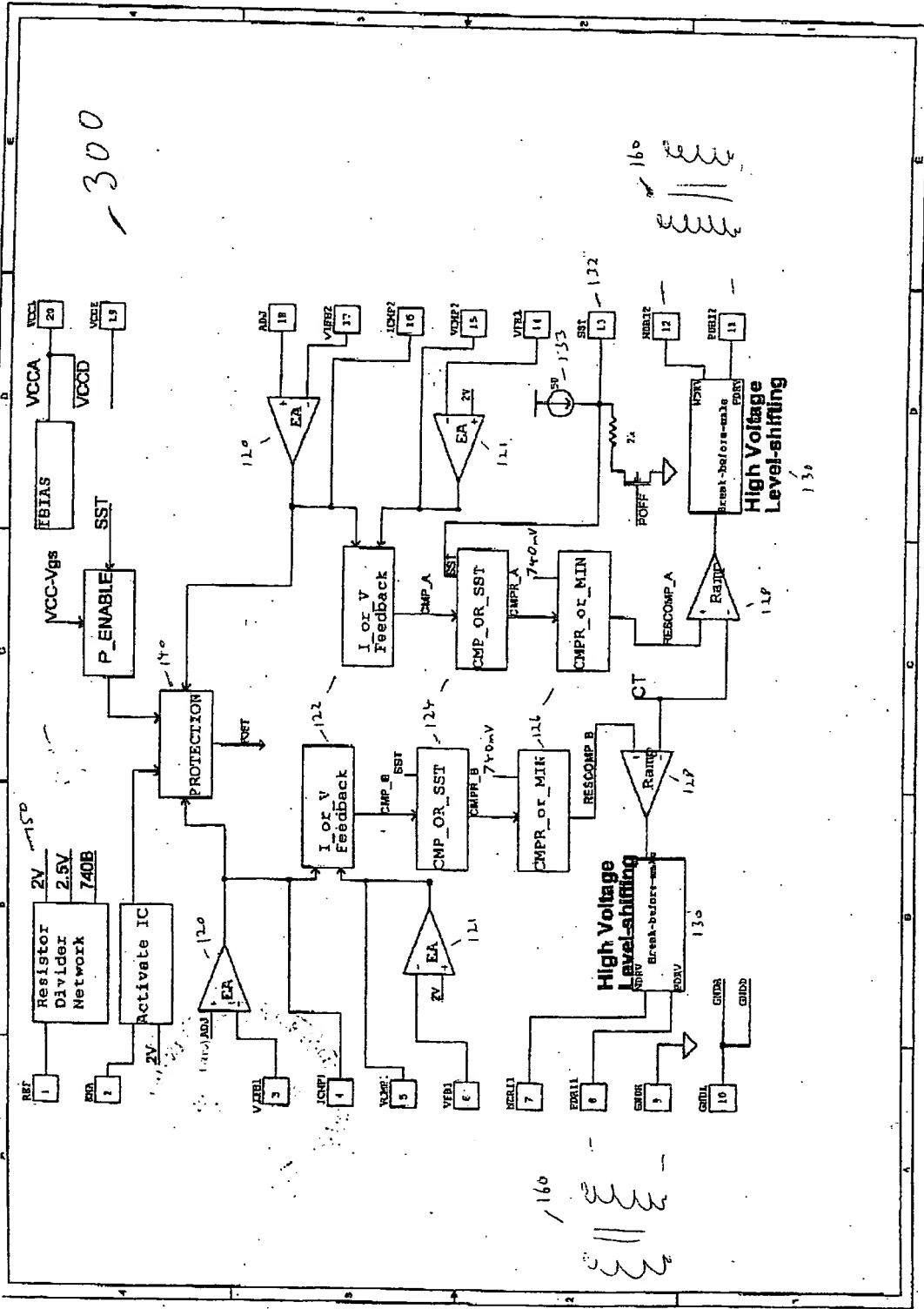


FIGURE 12

350

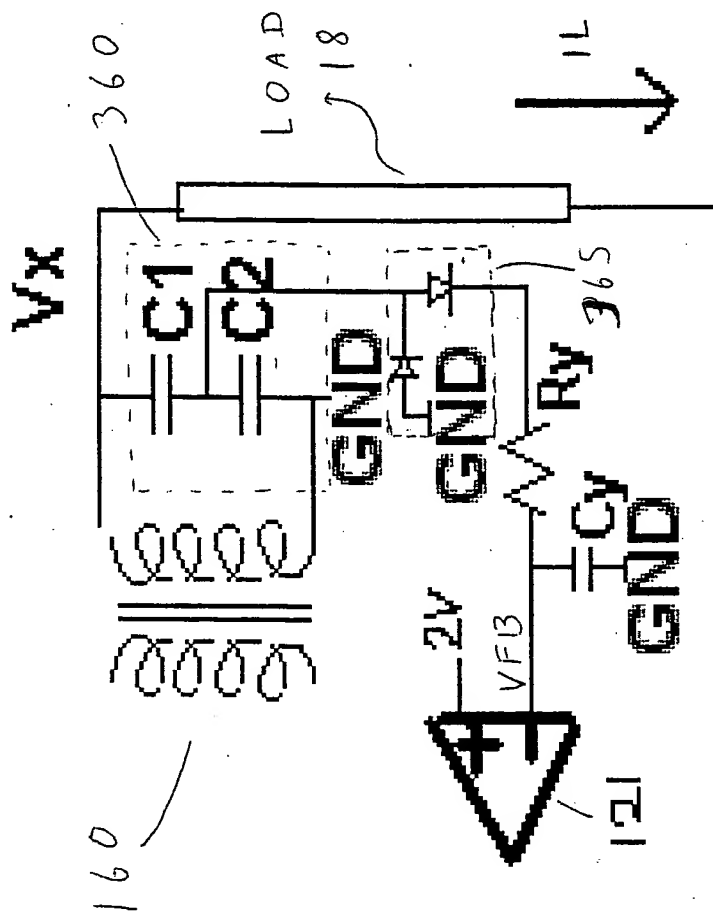


FIGURE 13

TOP SECRET

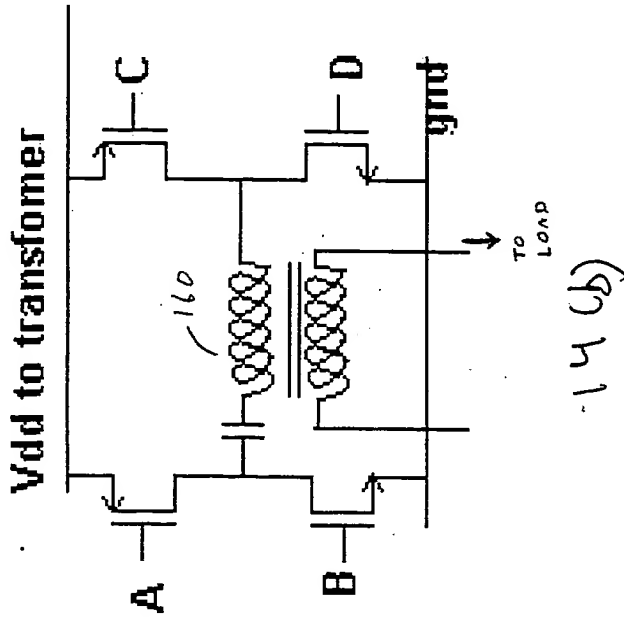
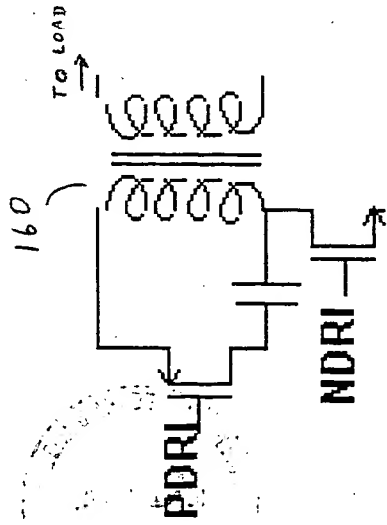


FIGURE 14

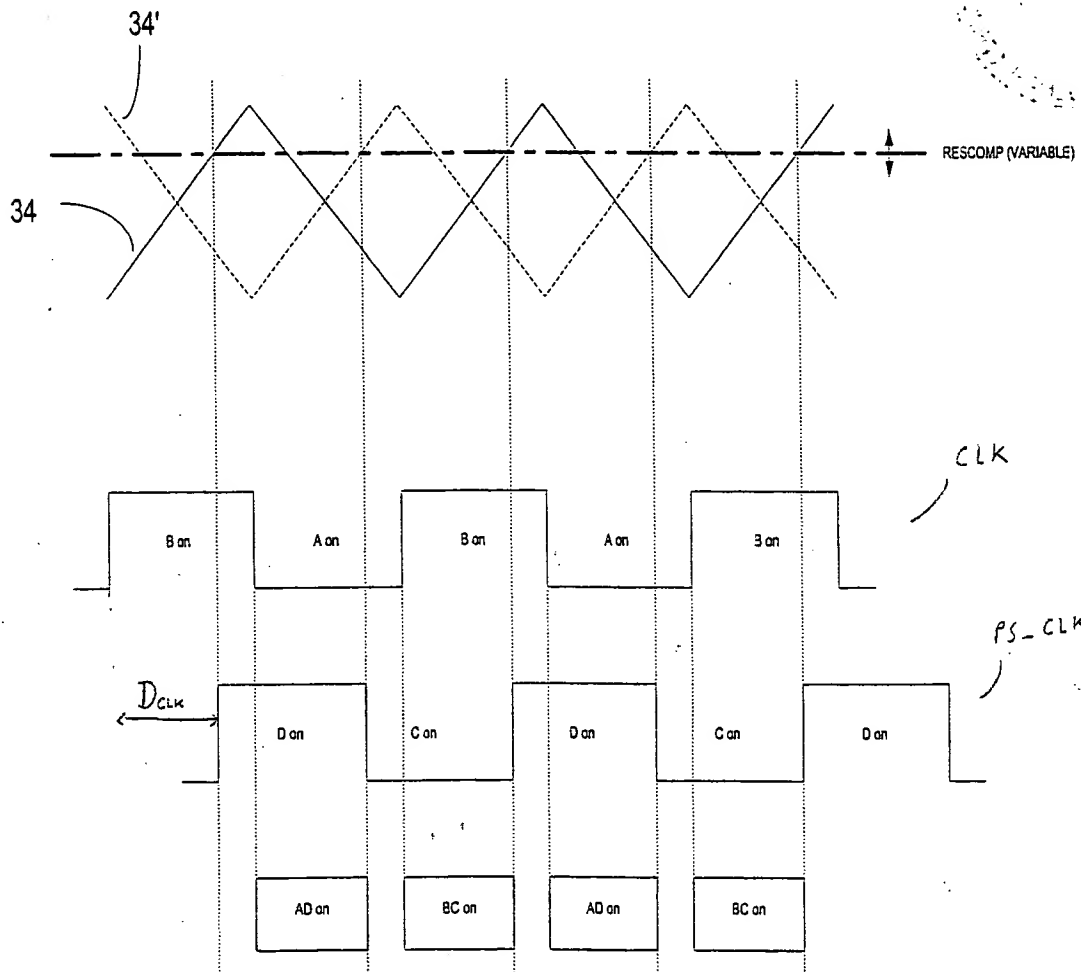


FIGURE 15